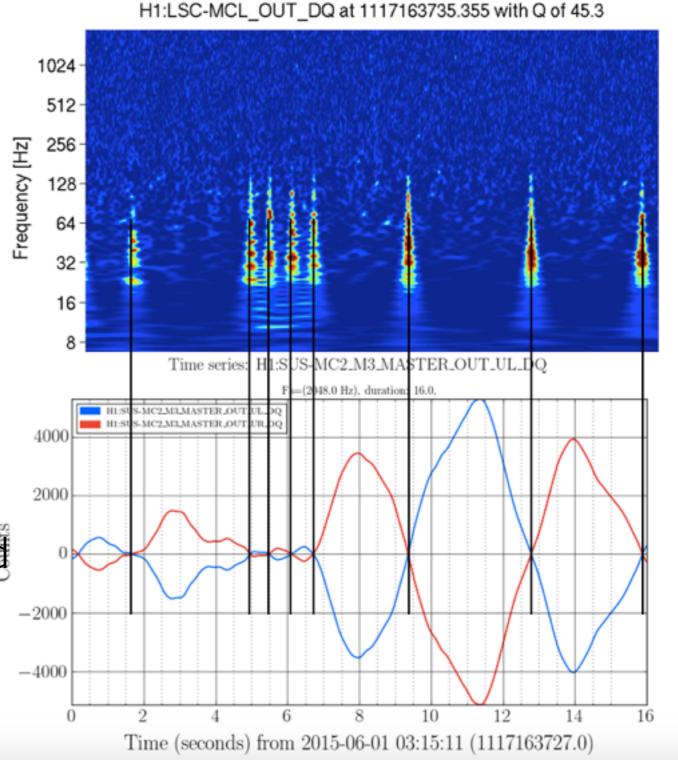
# LHO aLog

Edwin J. Son (NIMS)

## DAC zero crossing glitches

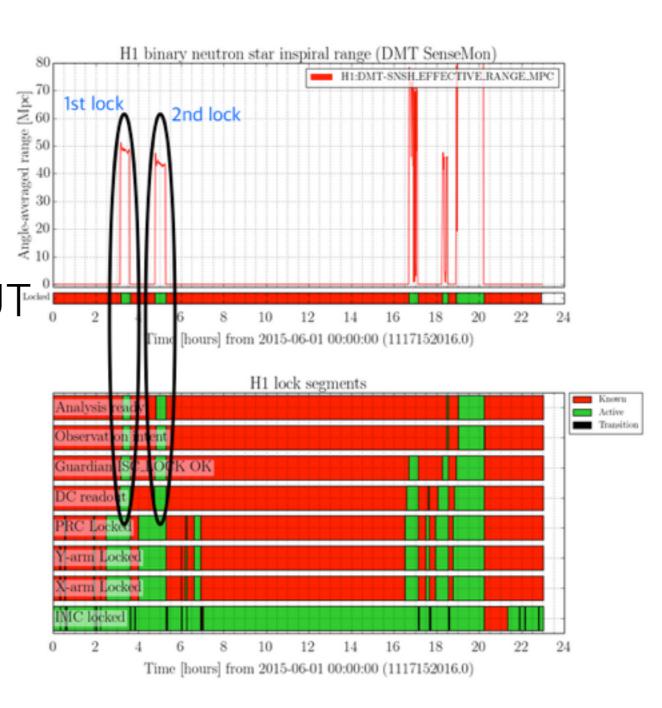
- https://alog.ligowa.caltech.edu/aLOG/ index.php?callRep=18739
- DAC glitches in MC2 M3
- The first plot is an Omega scan showing glitches in MC\_L, and the second shows that they correspond to zero crossings in MC2 M3 control.



## Common tidal saturating

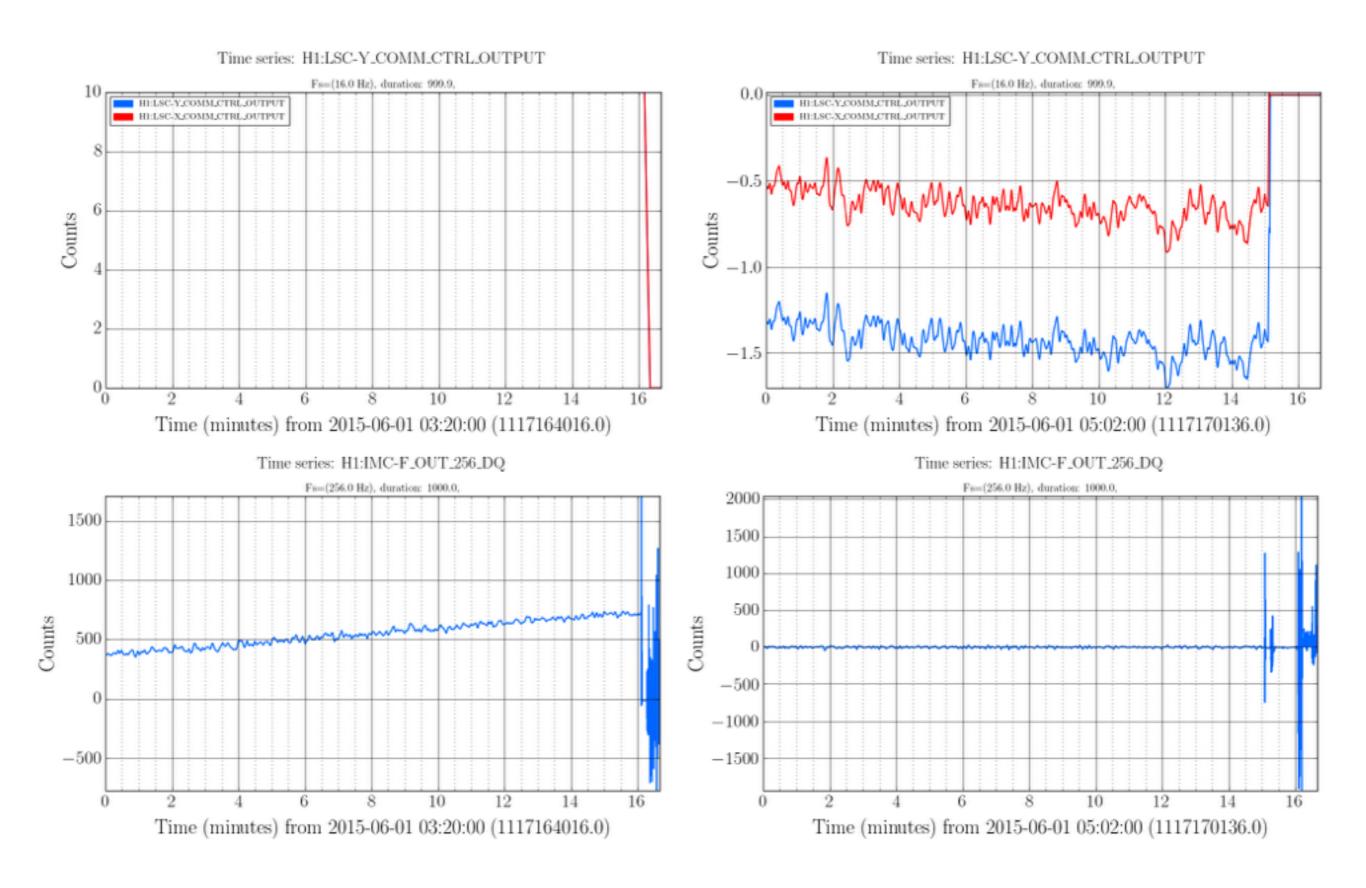
 https://alog.ligowa.caltech.edu/aLOG/ index.php?callRep=18748

LSC-Y\_COMM\_CTRL\_OUTPUT—
 and LSC X\_COMM\_CTRL\_OUTPUT at
 the end of the first short
 observation intent time are
 saturating at 10 um for this
 whole lock.

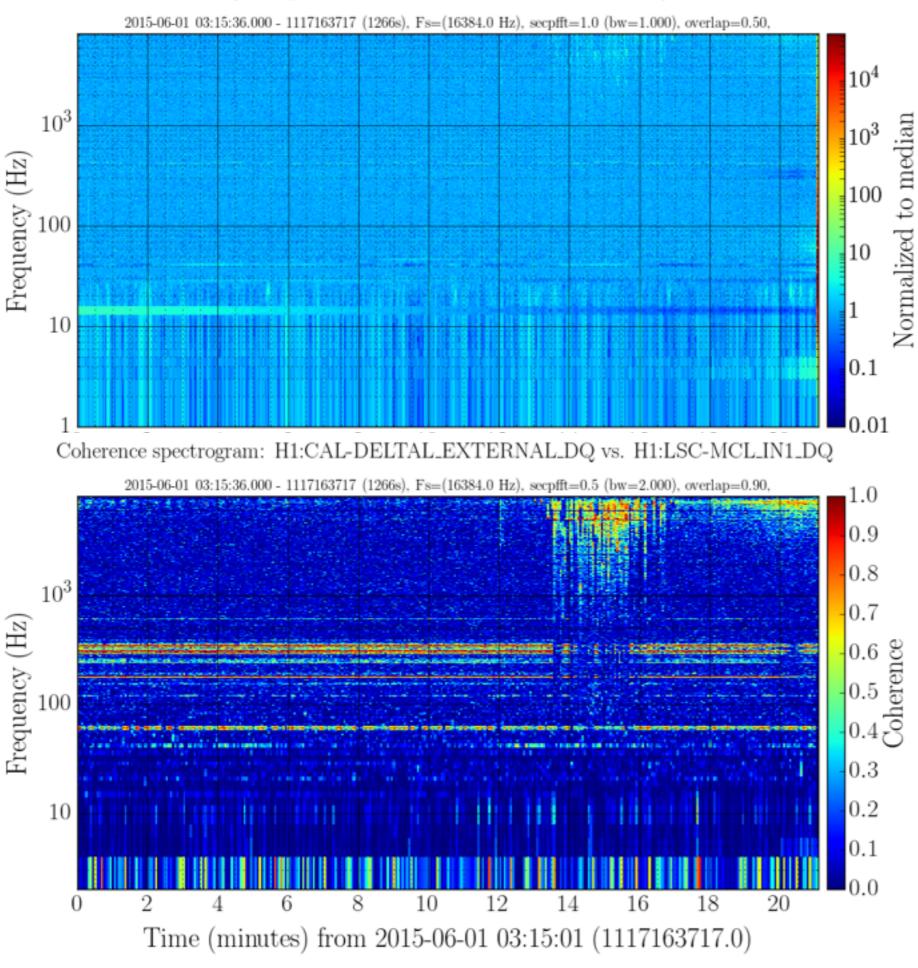


#### [1st lock]

### [ 2nd lock ]



#### Spectrogram: H1:CAL-DELTAL\_EXTERNAL\_DQ



- some additional noise during the first lock
- DARM
  coherence with
  MCL, PRCL, and
  SRCL also
  increases.

