Digital subgroup

- 5sets of digital system will be delivered to subgroups
  - Real time PC and software setup
  - Expansion chassis
  - ADC, DAC, Binary Output
  - Interface board to ADC/DAC/BO
  - Anti Alias/Anti imaging
  - whitening/dewhitening
  - Variable gain amp

- Seismic attenuation group
  - 1set with 2ADC, 2DAC
- Other group?
  - Input/output optics, Laser, Circuit, Vacuum

Schedule: Starts on the end of this FY

Signal interface matching

- ADC 32ch/card
  - D-sub 9pin(for 4ch, differential), +/-20V input
    - 16kHz sampling, may be decimated to 2kHz, 256Hz, 16Hz
  - or single end (not recommended, can be included your circuit; diagram provided)
- DAC 16ch/card
  - D-sub 9pin(for 4ch, differential), +/-10V outpuy
  - or single end (not recommended, can be included your circuit; diagram provided)
- BO 32ch/card
  - D-sub 9pin(for 4ch, differential), 0 or +5V output
    - Ex. Variable gain amp for 16 steps/1 D-sub connector
  - or single end (not recommended, can be included your circuit; diagram provided)

Reference:

http://gw.icrr.u-tokyo.ac.jp/JGWwiki/CLIO/Tasks/DigitalControl/pban_files