



Digital system

- Budget: 18MJPY for FY2011
- Standalone system for subsystems
 - Real time frontend: almost done with a new FREE OS at Kamioka building
 - Circuits: being designed by Altium compatible with LIGO CDS design, and ready for order for some parts
- Small network test with 2 or 3 PCs
 - Timing test
 - Reflective memory test: for control
 - DAQ network test: using Open-MX connecting between RT front-end and frame builder

FY		2010				2011				2012				2013				2014				2015				2016			
Quarter		1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q
Main Phase		Design				Tunnel				Vacuum				FPMI				RSE				Cryo							
Prototype test	CLIO operation	█	█	█	█																								
	Data analysys test							█	█	█	█	█	█																
Standalone system for subsystems	Hard/software setup					█																							
	Circuit					█	█	█																					
	Delivery								█																				
Article test	Small network test							█	█																				
	Large network test								█	█	█	█																	
Full system installarion	TOS→full system												█	█															
	Software setup															█	█												
	Cuircircuits																█	█											
	Newtork																				█	█							
Upgrage	RSE																												
	Cryo																											█	█